

c1 prior art matrices and by restricting routing in these derived prior art matrices to allow broadband transfers (Figure 4B); and

IN THE CLAIMS:

Amended the claims as follows:

Sub D1
C2
Claim 1. (Twice Amended) A device for switching ATM cells establishing a single path per virtual circuit, having N.R inputs and N.R outputs, N and R being two integers not less than two, the device comprising at least two stages, including an inlet stage comprising a plurality of matrices (21; 31; 411₁, ..., 411_R) and having R.N sets of Q outputs (213₁₁; 313₁₁; 413₁₁) and an outlet stage comprising a plurality of matrices (22; 33; 421₁, ..., 422₁, ...) and having R.N sets of Q' inputs (222₁; 332_{1,1}; 423_{1,1}),

characterized in that for the flow of data carried by any intermediate link (213_i, 222_j; 313_i, 332_j, 413_i, 423_j) that is part of the single path set up between an input and an output to be a subset of the incoming flux at that input and also a subset of the outgoing flux at that output, each input (212₁; 312₁; 412₁) of the inlet stage can be connected to an output of the inlet stage which can be selected only from Q outputs (213₁₁, ..., 213_{R1}; 313₁, ..., 313_{1R}; 413₁₁, ..., 413_{1R}) exclusively associated with that input; and

in that each output (223₁; 333₁; 442₁) of the outlet stage can be connected to an input of the outlet stage which can be selected only from Q' inputs (222₁₁, ..., 222_{1R}; 332₁₁, ..., 332_{R1}; 423₁₁, ..., 423_{1R}) of the outlet stage exclusively associated with that output; and

AMENDMENT UNDER 37 C.F.R. § 1.116

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c2 further configured so that the flow of data at each input of the inlet stage can be directed
to each matrix of the outlet stage
